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APPLICANT: HUEMOELLER ET AL.

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TITLE:

IMPRINTED INTEGRATED CIRCUIT SUBSTRATE AND METHOD

FOR IMPRINTING AN INTEGRATED CIRCUIT SUBSTRATE

Commissioner for Patents P.O. Box 1450

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May 28, 2004

REQUEST FOR STATUS

Dear Sirs:

Please give us the status on the above-identified Patent Application.

Please advise us of the situation as soon as possible. Respectfully submitted,

Jeffrey D. Moy Reg. No. 39,307

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JDM/sib